

Title: Autotransformer-Based System and Method
of Current Harmonics Reduction in Inverter Circuit
Inventor(s): Girish R. Kamath
Appn. No. Not yet assigned
Docket # 37125/31374

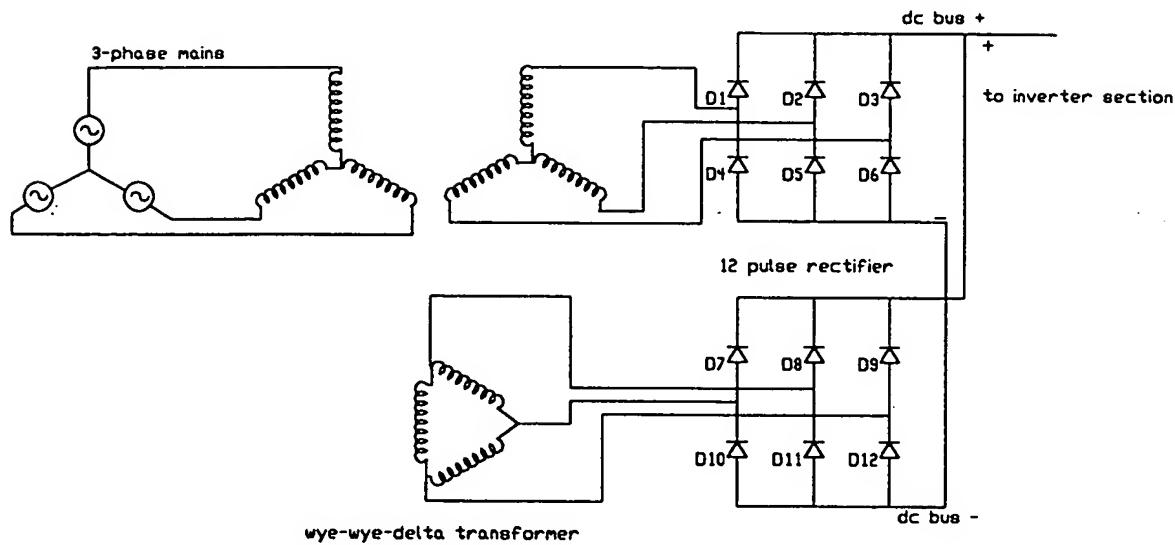


Fig. 1
Prior Art

Title: Autotransformer-Based System and Method
of Current Harmonics Reduction in a Circuit
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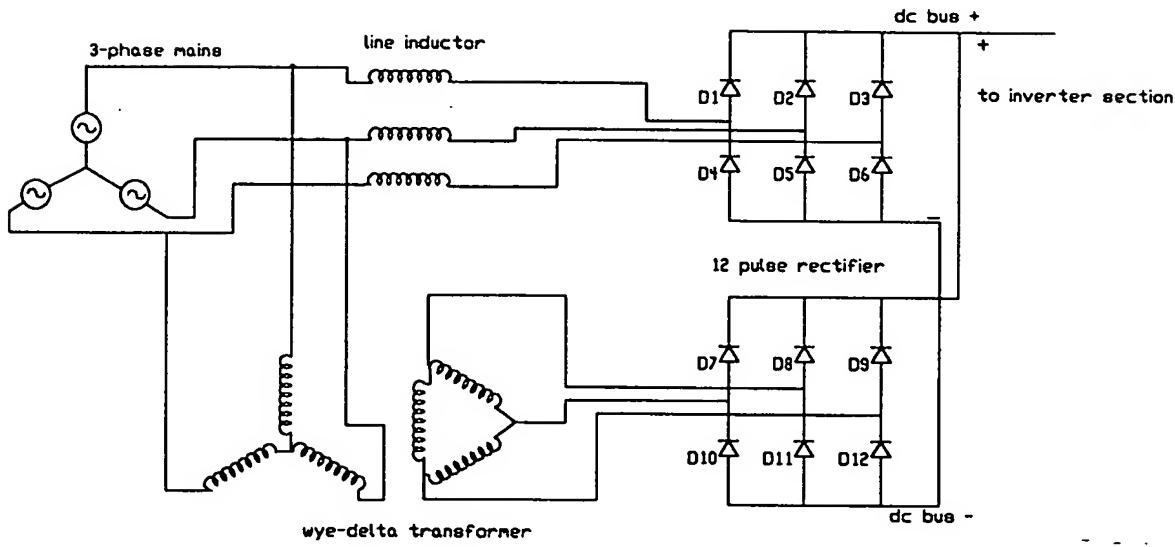


Fig. 2
Prior Art

Title: Autotransformer-Based System and Method
of Current Harmonics Reduction in Power Circuit
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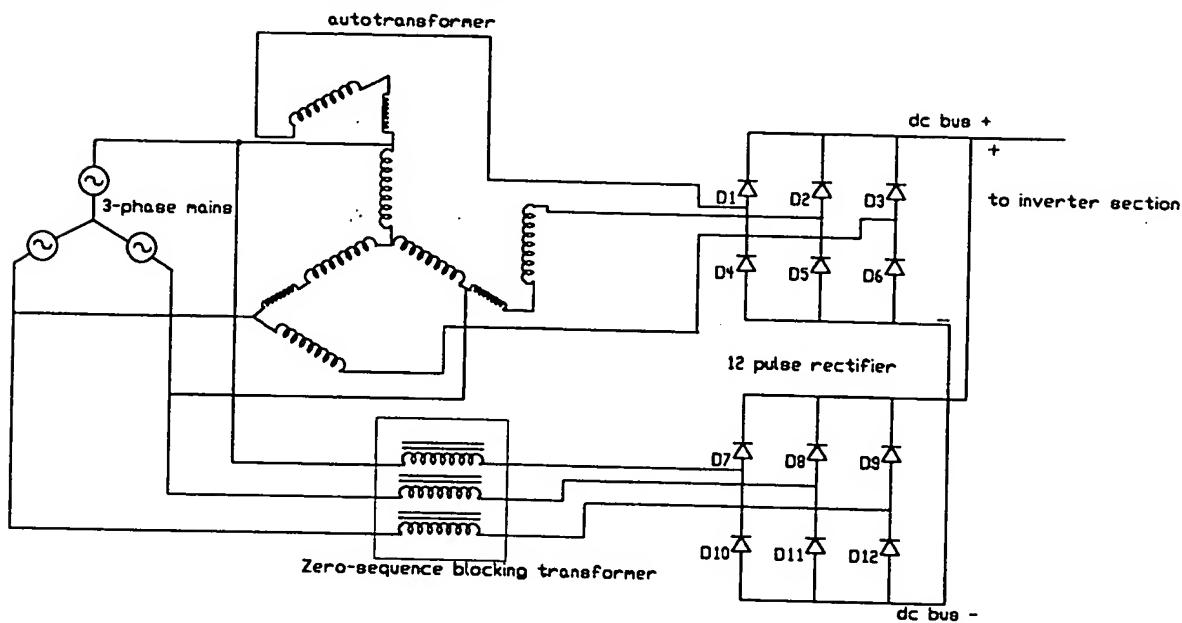


Fig. 3
Prior Art

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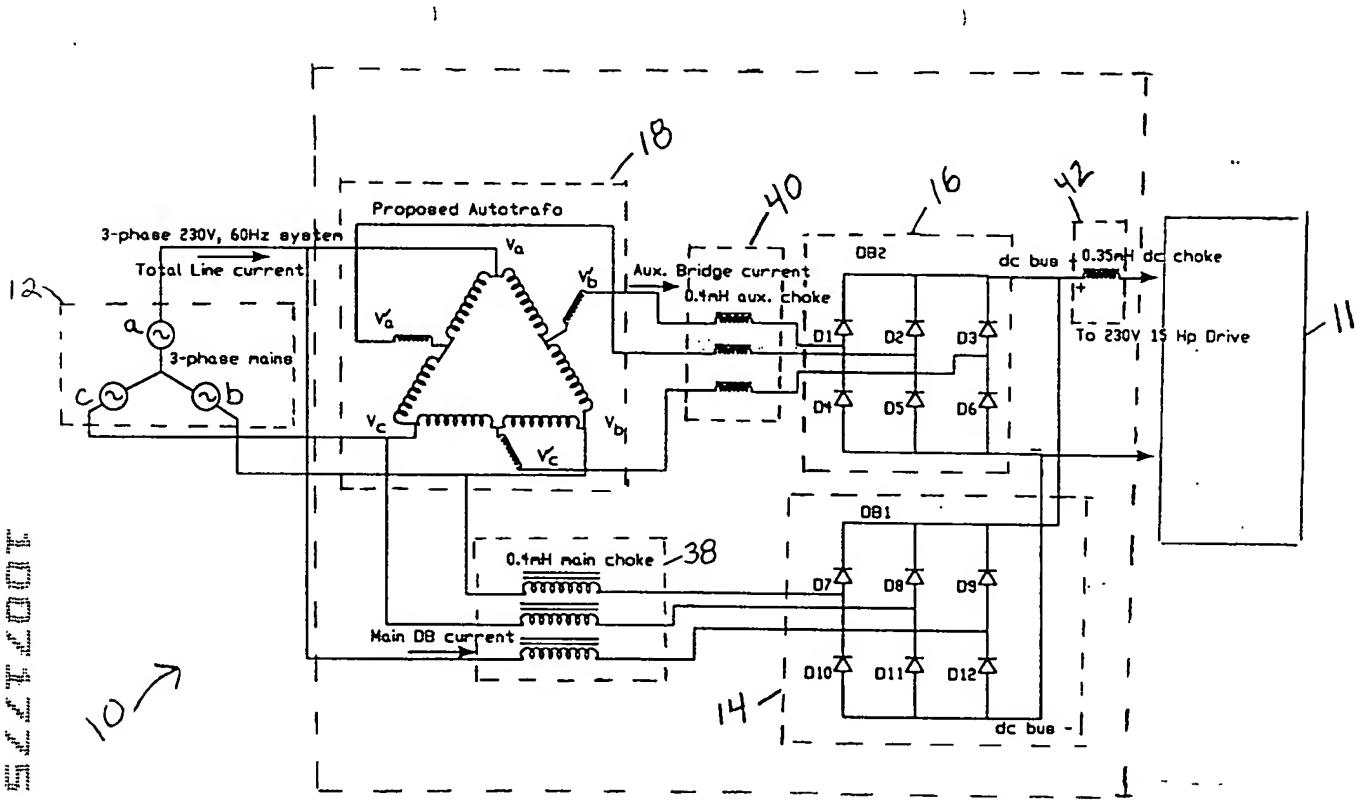


Fig. 4

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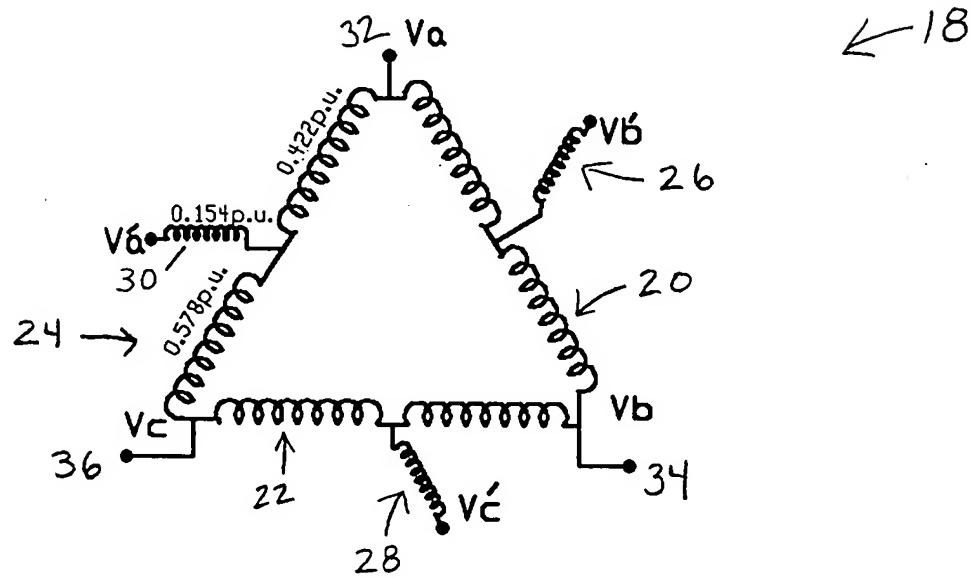


Fig. 5

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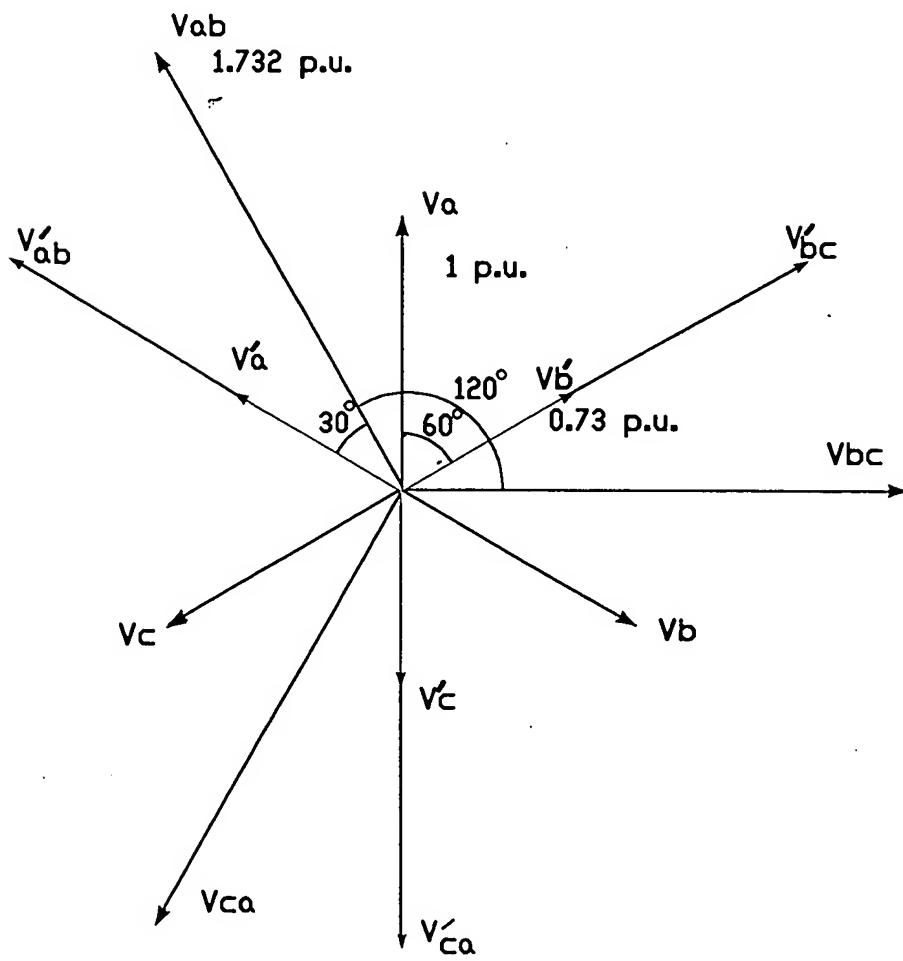


Fig. 6

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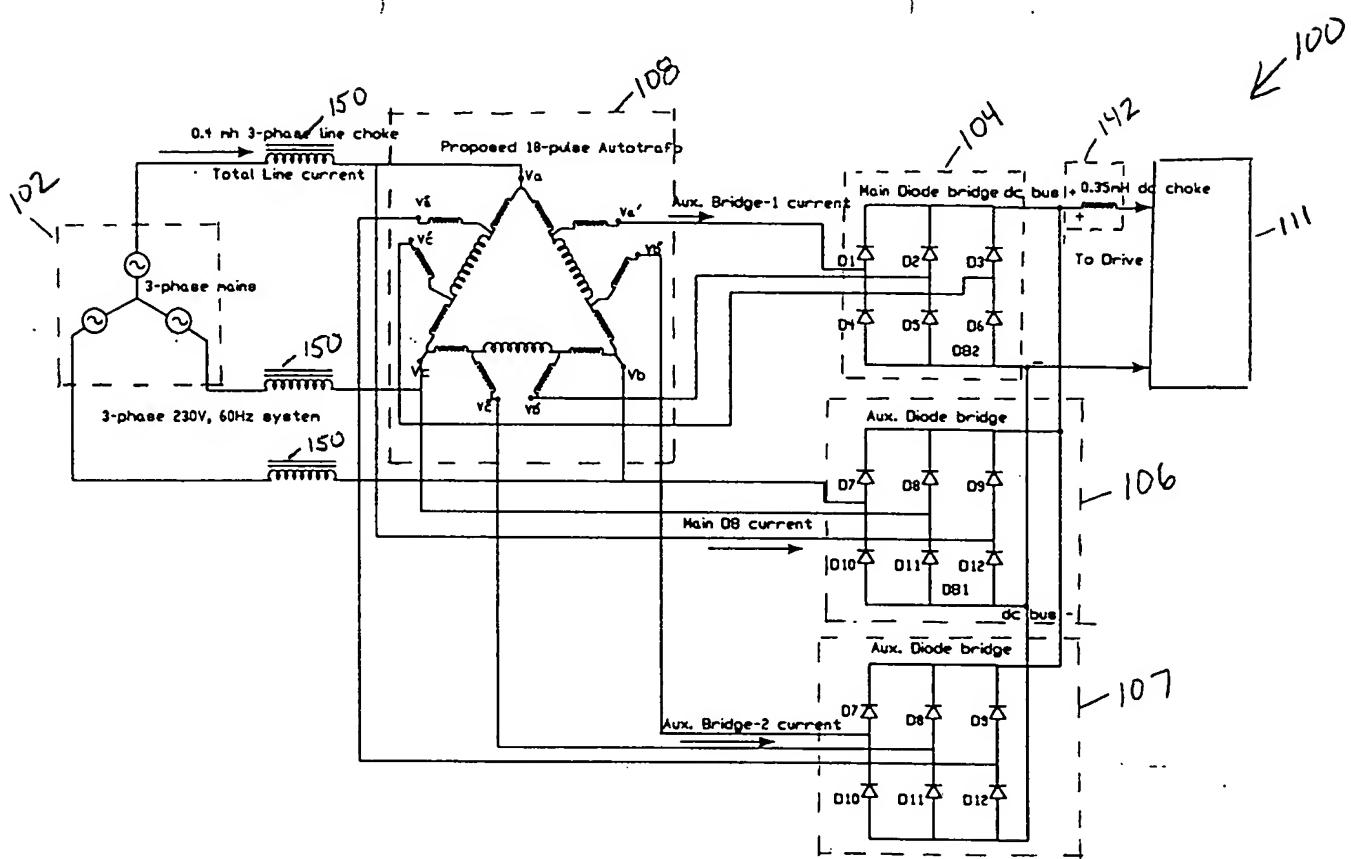


Fig. 7

Title: Autotransformer-Based System and Method
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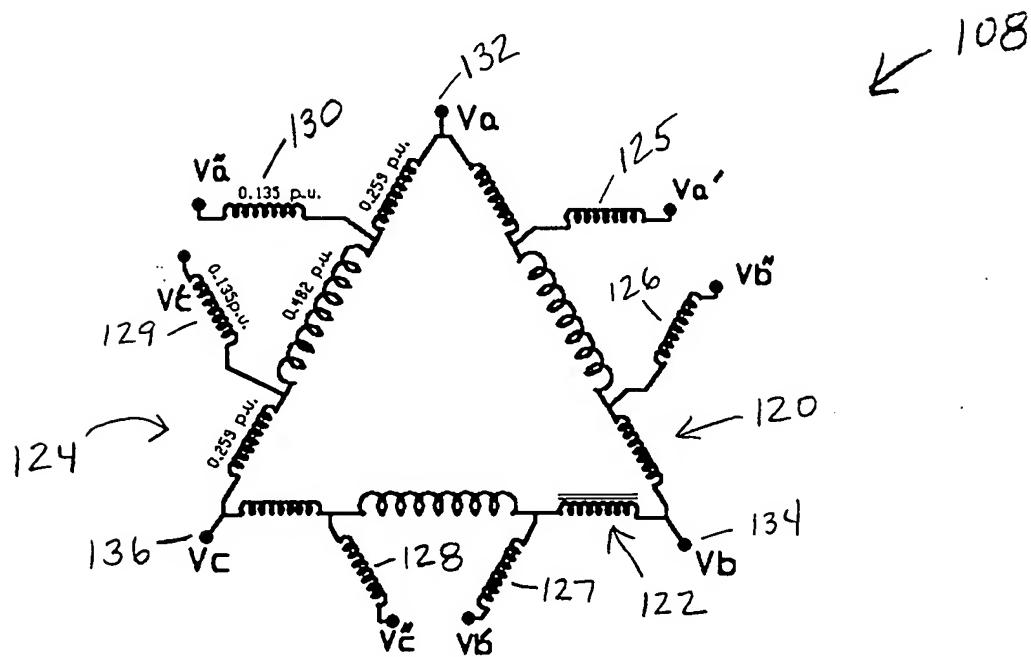


Fig. 8

Title: Autotransformer-Based System and Method
of Current Harmonics Reduction in [REDACTED] circuit
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Normalized Transformer power ratings

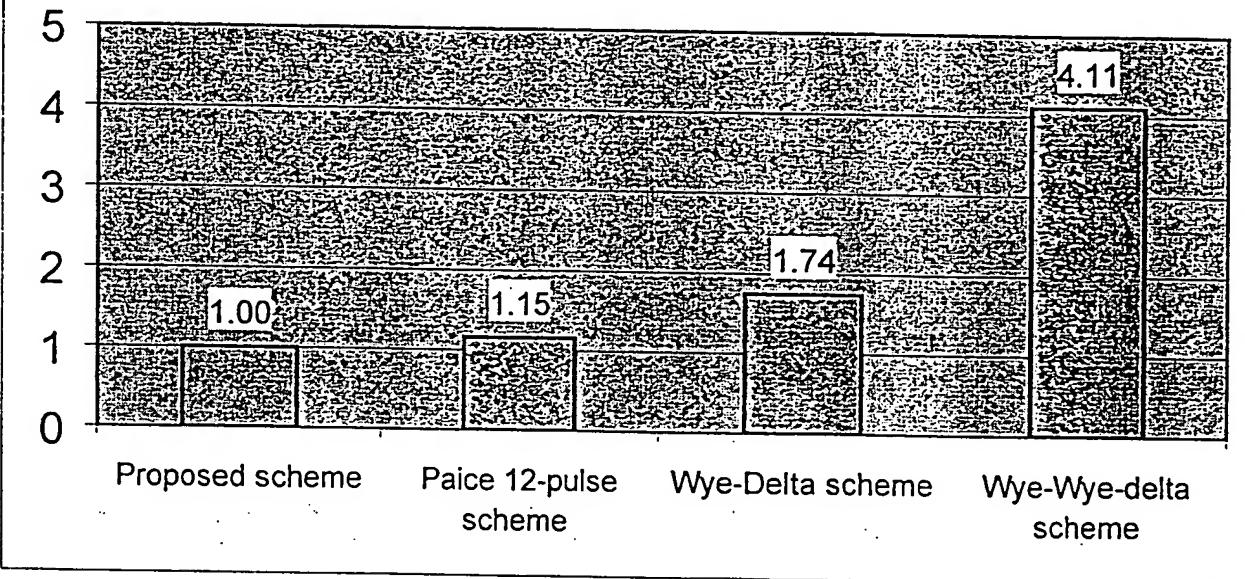


Fig. 9

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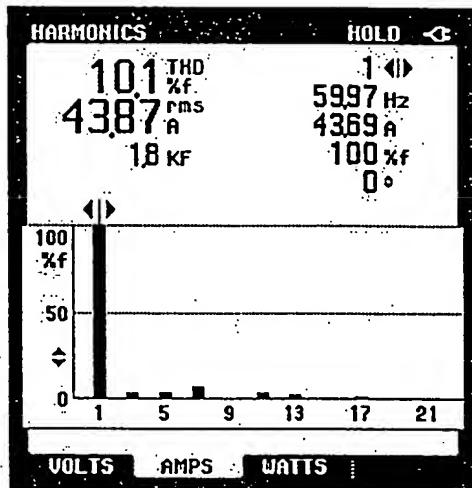
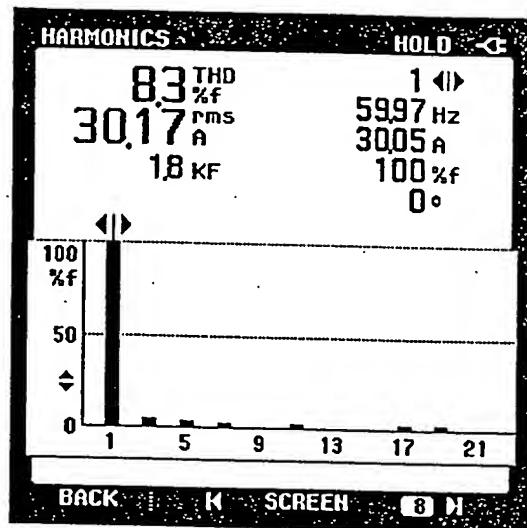


Fig. 10

Title: Autotransformer-Based System and Method
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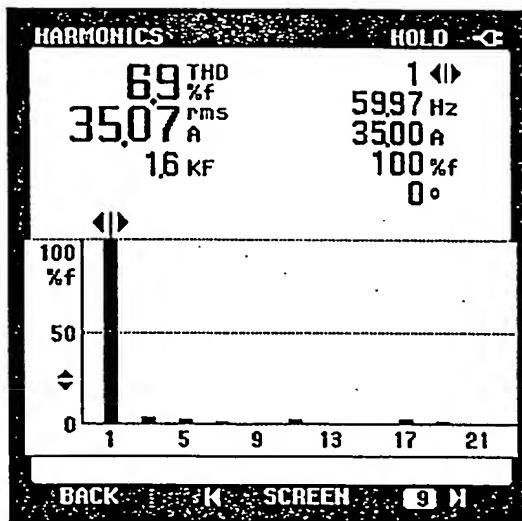


Fig. 11B

100-225-820-200-200